# Quadri-phase Shift Keying (QPSK)

#### **Review: Binary Phase Shift Keying Modulation and Demodulation**



# Quadri-phase Shift Keying (QPSK): Signal Representation

"1 1" 
$$s_1(t) = A\cos(2\pi f_c t - \pi/4) = +\frac{A}{\sqrt{2}}\cos(2\pi f_c t) + \frac{A}{\sqrt{2}}\sin(2\pi f_c t)$$
  
"1 0"  $s_2(t) = A\cos(2\pi f_c t + \pi/4) = +\frac{A}{\sqrt{2}}\cos(2\pi f_c t) - \frac{A}{\sqrt{2}}\sin(2\pi f_c t)$   
"0 0"  $s_3(t) = A\cos(2\pi f_c t + 3\pi/4) = -\frac{A}{\sqrt{2}}\cos(2\pi f_c t) - \frac{A}{\sqrt{2}}\sin(2\pi f_c t)$   
"0 1"  $s_4(t) = A\cos(2\pi f_c t + 5\pi/4) = -\frac{A}{\sqrt{2}}\cos(2\pi f_c t) + \frac{A}{\sqrt{2}}\sin(2\pi f_c t)$   
 $s_{QPSK}(t) = d_I \frac{A}{\sqrt{2}}\cos(2\pi f_c t) + d_Q \frac{A}{\sqrt{2}}\sin(2\pi f_c t)$   
 $d_I = \begin{cases} 1 & \text{if } b_{2i-1} = 1 \\ -1 & \text{if } b_{2i-1} = 0 \end{cases}$   
Even  $d_Q = \begin{cases} 1 & \text{if } b_{2i} = 1 \\ -1 & \text{if } b_{2i} = 0 \end{cases}$   
 $bits$   
 $s_{QPSK}(t) = A_k \cos(2\pi f_c t) + B_k \sin(2\pi f_c t)$ 

 $s_{QPSK}(t) = Binary PSK on cos(2\pi f_c t) + Binary PSK on sin(2\pi f_c t)$ 

- In this type of modulation two binary digits are grouped together to form one message that phase modulates the carrier  $Acos(2\pi f_c t)$ .
- The transmitted signal assumes one of four possible phases (+ 45, -45, +135, -135)  $Acos(2\pi f_c t + \theta_i)$
- A QPSK signal can be decomposed into a sum of two PSK signals; an inphase component and a quadrature component. The serial to parallel converter splits the incoming data sequence into two sequences that consist of the odd and even bits of the main sequence. The odd bit stream sequence modulates the in-phase carrier, while the even bit stream sequence modulates the quadrature carrier.

# Quadri-phase Shift Keying (QPSK): Signal Representation

$$s_{QPSK}(t) = d_{I} \frac{A}{\sqrt{2}} \cos(2\pi f_{c}t) + d_{Q} \frac{A}{\sqrt{2}} \sin(2\pi f_{c}t)$$

$$d_{I} = \begin{cases} 1 & \text{if } b_{2i-1} = 1 \\ -1 & \text{if } b_{2i-1} = 0 \end{cases}$$

$$d_{Q} = \begin{cases} 1 & \text{if } b_{2i} = 1 \\ -1 & \text{if } b_{2i} = 0 \end{cases}$$

 $s_{OPSK}(t) = A_k cos(2\pi f_c t) + B_k sin(2\pi f_c t)$ 

 $s_{OPSK}(t) = Binary PSK on cos(2\pi f_c t) + Binary PSK on sin(2\pi f_c t)$ 

- The serial to parallel converter splits • the incoming data sequence into two sequences that consist of the odd  $(A_k)$  and even bits  $(B_k)$  of the main sequence. The odd bit stream sequence modulates the in-phase carrier, while the even bit stream sequence modulates the quadrature carrier.
- Both in phase and quadrature BPSK are transmitted over the same bandwidth.



### Quadri-phase Shift Keying (QPSK): Modulation



# Quadri-phase Shift Keying (QPSK): Demodulation



# **QPSK:** Probability of Error

• The symbol error probability is twice the bit error probability, and is given as (will also be derived in the next chapter when we consider M-ary PSK).

$$P_b^* = Q\left(\sqrt{\frac{2E_b}{N_0}}\right) = Q\left(\sqrt{\frac{E_s}{N_0}}\right)$$

 This is the same as that for binary PSK, provided that both message bits have the same energy. The advantage of QPSK is that it is more bandwidth efficient than BPSK (can transmit twice the data rate within the same bandwidth)

#### **QPSK: Power Spectral Density and Bandwidth**

The power spectral density has the same shape as that for BPSK (the QPSK is the sum of

two BPSK signals one modulated on  $cos(2\pi f_c t)$  and the other on  $sin(2\pi f_c t)$ .

Remember that the symbol duration τ is twice the bit duration.



 $s_{QPSK}(t) = Binary PSK on cos(2\pi f_c t) + Binary PSK on sin(2\pi f_c t)$ 

Note that for QPSK, the data rate is  $R_b$  bits/sec and the B.W =  $R_b$  Hz Which means we can transmit  $2R_b$  bits/sec and the B.W =  $2R_b$  Hz While for regular BPSK, when the data rate is  $R_b$  bits/sec the B.W =  $2R_b$  Hz Hence, QPSK is more bandwidth efficient than BPSK since in W Hz, we can transmit W bits/sec while in BPSK we can transmit half that value W/2 bits/sec for the same probability of error.